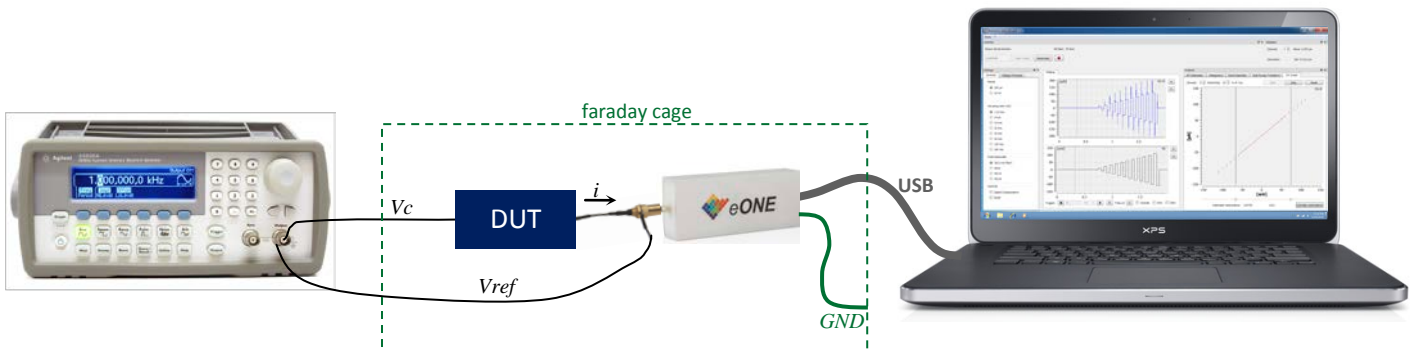


CONNECTION SCHEME FOR eONE AND EXTERNAL FUNCTION GENERATOR



NOTE: DO NOT CONNECT GND TO V_{ref} ! They are at different potentials.

PROCEDURE:

- 1- connect the parts as indicated in the picture;
- 2- in EDR, set $V_{hold} = 0$ using protocol 1;
- 3- set the test wave on the function generator (i.e. a sinusoidal wave);
- 4- modify the Voltage Offset on the function generator, to center the current wave on EDR to the 0.

NOTE: this procedure works for resistive DUT (Device Under Test). If you have other DUT, like pure capacitors, you need to compensate the offset of the function generator with a resistor before to plug the DUT.